

Department of ECE Electronics & Communication Engg.,

Electronics &
Communication
BUZZ.....



Special points of interest:

- ☞ International day of people with disabilities
- ☞ A Guest Lecture on “Advances in Avionics and Aerospace Electronic Systems”
- ☞ A Five Day Workshop on “ Analog Integrated Circuit Design and ASIC Implementation using CADENCE EDA Tools “

International Day of persons with Disabilities

International day of people with disability is a United Nations sanctioned day that aims to promote an understanding of people with disability and encourage support for their dignity, rights and well-being. On this occasion every year the management of SVES is donating assistive technology projects to the differently abled people.



Dignitaries on the Dais

During the past four years more than 150 items have been distributed by the management, Sri Vishnu Educational Society, Bhimavaram for the needy people.

The following projects are designed in ATL and distributed to clients
1. robotic metal feeder 2. Smart math trainer 3. Bubble tube 4. JHealth mentoring system 5. Versatile wheel chair 6. Interactive wrist band 7. Succour 8. third eye. Clients from CVC Bhimavaram, Sri Venkateswara Deaf School, Arundaya Manovikasa Kendram received the projects. This year’s event is little different because we have finally come up with a product E-Stick for the benefit of our visually challenged clients. Mr. Mohan Rao from Bhimavaram government Hospitals was the chief guest for the function. Dr. G Srinivasa Rao principal ,Dr. Padamavasavi , Dean ATL , HODs and staff of different departments attended to this event.



Exhibition of Projects to Clients

Inside this issue:

Faculty publications & Training	2
IETE Activities	3
ISTE Activities and Guest Lectures	3
Workshops	4

Faculty Training & Publications

1. G.R.L.V.N.S. Raju and G.D.V. Kanakadurga, "Development of SDC-SDF Architecture for Radix-2 FFT," National Conference on Recent Advances in Communication & Electronics Engineering, 21-22 Oct 2016.
2. Sudheer Kumar Terlapu, GRLVN SrinivasaRaju and GSN Raju, "Array Pattern Synthesis using Flower Pollination Algorithm," Proceedings of IEEE International Conference on Electromagnetic Interference and Compatibility INCEMIC 2016 organized by Society of EMI/EMC, during 8-9 December 2016
3. Sudheer Kumar Terlapu, GRLVN SrinivasaRaju and GSN Raju, "Design of Linear Array for Shaped Beams using Accelerated Particle Swarm Optimization for Marine Radar Applications," Proceedings of a Two-Day National Convention on "Recent Advances in Microwave, Antennas and communications" during 1st and 2nd July, 2016 Organized by Department of Electronics and Communication Engineering in association with The Institution of Engineers (India), at PVP Siddhartha Institute of Technology, Vijayawada.
4. Sudheer Kumar Terlapu and GSN Raju "Synthesis of linear array for side lobe reduction using practical swarm optimization" 1st International conference on Sustainable computing Techniques in Engineering, Science and Management (SCESM 2016) organized by SN Educational Society and URGCEE,USA and hosted by Hierank Business school, Noida, India, during 9th and 10th September 2016.
5. P. Ambica Devi, V. Srinivasa Rao,"IMPLEMENTATION OF AN EFFICIENT CLASS-AB OP-AMPS WITH HIGH AND SYMMETRICAL SLEW RATE USING H-SPICE "5th International Conference on Science, technology and Management (ICSTM),India International Center ,New Delhi,ISBN:978-93-86171-00-9,30th July 2016.
6. V Srinivasa Rao, Rajesh K Panakala, Rajesh Kumar Pullakura,"Implementation of A 2d-DWT System Architecture For Jpeg2000 Using Matlab And DSP"2016 International Conference on computation system and Information Technology for Sustainable Solutions(CSITSS),Pages:86-90,DOI:10.1109/CSITSS.2016.7779446,IEEE Conference Publications,Oct,2016.
7. V Srinivasa Rao, Rajesh K Panakala, Rajesh Kumar Pullakura, "Image Compression using DWT" International conference ICRISHEM-2016, at G V P college of Engineering, Visakhapatnam on 21-22 August 2016.
8. V Srinivasa Rao, Rajesh K Panakala, Rajesh Kumar Pullakura,"AN EFFICIENT 2D-DISCRETE WAVELET TRANSFORM ARCHITECTURE FOR JPEG 2000 USING FIELD PROGRAMMABLE GATE ARRAYS" Fifth International Conference on Recent Trends in Information Processing & Computing IPC-2016 ,Bangalore,November 12-13,2016.
9. V Srinivasa Rao, Rajesh K Panakala, Rajesh Kumar Pullakura,"An Area Efficient And High Speed EBCOT For JPEG2000 Using Cadence EDA Tools ",International Conference ,ICAMT – Elsevier Materials Today: Proceedings at dadi Institute of Technology ,Anakapalli,Visakhapatnam,27-28 December,2016.
10. M Prema Kumar and P Rajesh Kumar, "Particle Swarm Optimization Algorithm for Enhanced image fusion in Mammography" International conference on Recent Trends in Engineering, Technology and science, (ICRETS 2016) August 2016.
11. A.Srivalli, M.Prema Kumar, "Area Efficient Multiplier Design using Fixed Width Replica Redundancy Block" International conference on "Recent Trends in Engineering Technology and Sciences(ICRETS). pp: 74-79, August 2016
12. M Pradeep, et al., "High Performance VLSI architecture for radix-2 motogometry modular multiplication" in ICRETS-2016, at S T J Institute of Technology, Ranebemur on 12-13 August 2016.
13. M.V Ganeswara Rao, Rajesh K Panakala, AM Prasad "Image Processing using FPGA-A Frame Work", international conference on sustainable computing techniques in engineering , science and management (SCESM-201, organized by HIERANK Business School, Noida during 09-10 September 2016.
14. Swathi Samanthapudi, Murali Krishna D, "Efficient Multiplication for the DSP Applications using Static Segment Method", International Conference on research Challenges in Engineering and Technology on 18-19 August, 2016.
15. G.R.L.V.N.S. Raju and G.Deeshma Venkatanakadurga, "Implementation of SDC-SDF Architecture for Radix-4 FFT," International Journal of VLSI design & Communication Systems (VLSICS) Vol.7. No.4, August 2016.

Students Activities & Achievements

IETE Activities

1. IETE STUDENT'S FORUM conducted an event named 'TICKLE YOUR BRAIN'. It's a non-technical event conducted to I & II year students. It's a WRITTEN event which contains few fill in the blanks for which they has to fill in the appropriate answers. The questions of this event were entirely based on BRAIN BATS. The total no. of participants in this event is 160 members.
2. IETE STUDENTS' FORUM conducted an event named (HOPE)- HOBBY PROJECT EXPO. All the students of 3rd year ECE took part in this event. Students are divided into a team of three to four members. They presented a project based on their interest. Each team is assigned a mentor from faculty of ECE department. The best project will be the winner.



A Guest Lecture on Advances in Avionics and Aerospace Electronic Systems

Dr. M. R. Nayak delivered a talk on Advances in Avionics and Aerospace Electronic Systems on 30th September, 2016. Dr. M.R.Nayak interacted with Students, showed pictures related to technology linked with aerospace and marine and created curiosity in students to learn more. Later he interacted with faculty members and suggested several research areas and funding opportunities.



ISTE Activities

1. Engineers Day was celebrated on 15th September, 2016 as a tribute to Bharat Ratna Shri M. Visvesvaraya, on his birth anniversary.

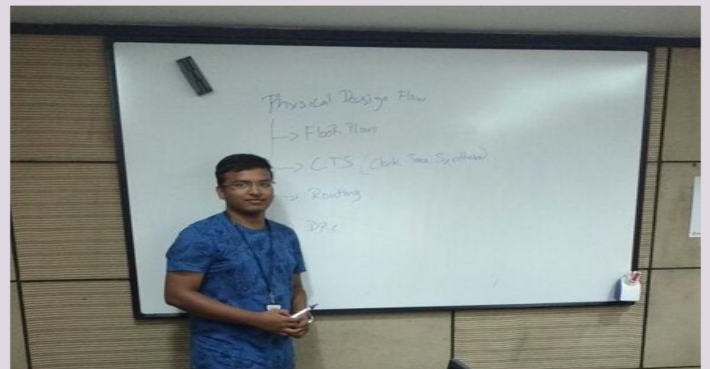


2. ISTE Student Chapter of SVECW organized various technical events on 25th September 2016. An ICE Breaking Program was conducted on this occasion. The events such as Essay Writing, Quiz Prelims, Drawing, Group Discussion were also conducted.



A Guest Lecture on Advancements in System on Chip Report

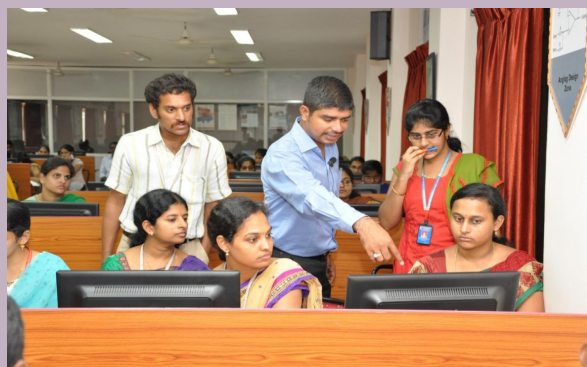
A guest lecture is conducted by the Department of ECE on "Advancements in System on Chip Report" on 22/10/2016 by Mr. K. Anil Kumar, Design Engineer, SOC Tronics, Hyderabad. The primary objective of this programme is to bridge theoretical concepts



*A Five Day Workshop on
“ Analog Integrated Circuit Design and ASIC Implementation using CADENCE EDA Tools “*

A five day workshop was conducted by dept. of ECE from 06th to 10th June, 2016. On 6th June, the proceedings began with inauguration ceremony, with the esteemed presence of Honourable Principal Dr. G. Srinivasa Rao, Convenor & and HOD of ECE Department Dr. G.R.L.V.N Srinivas Raju , along with coordinators Mr V.Srinivasa Rao & Mr.M.Pradeep.

From Introduction on CIC Design Flow and ASIC Design flow to Understand the concepts of Floor-planning, Power Planning, Placement , CTS, Routing the session continued successfully with hand-son experience with CADENCE EDA tool.



Prof. GRLVNS Raju
Head of the Dept

The Department of Electronics and Communication Engineering of Shri Vishnu Engineering College for Women (SVECW) was established in the academic year 2001-02 with an annual intake of 60. The Department is accredited by NBA (National Board of Accreditation, AICTE, New Delhi) for 5 years. The intake was increased to 120 from the academic year 2004-05 and later the intake was increased to 180 from the academic year 2011-12. In addition to this intake, the Department has 20% lateral entry students at II B.Tech. level.

The B.Tech. (ECE) program is duly approved by the AICTE and Government of Andhra Pradesh and has a permanent affiliation to Jawaharlal Nehru Technological University (JNTU), Kakinada. The Department has a number of well-equipped laboratories and provides excellent facilities for learning. The Department periodically organizes seminars, symposia, workshops and guest lectures for the benefit of both the students and the faculty. The department has an excellent blend of well experienced and young faculty.



“The Road to Success is dotted with many tempting parking spaces”

-Will Rogers