

SHRI VISHNU ENGINEERING COLLEGE FOR WOMEN

(Autonomous)

Department of Electronics & Communication Engineering
Vishnupur, Bhimavaram - 534202

VLSI Laboratory

Lab-In-Charge : Mr. R. Ashok Chaitanya Varma

Lab-Technician : Mr. N.V.V. Satish Kumar

Objective:

The objective of the VLSI Laboratory is aimed to bridge the gap between academics and industry based on well-known **Cadence design systems**. The students are given advanced training to get equipped with latest and modern needs. The UG and PG programs are designed to give an in-depth training and knowledge in the areas of Application Specific Integrated Circuits (ASIC).



LAB EQUIPMENT:

S.No.	Item Description	Qty
1	FPGA kits for SPARTAN-II	5
2	ALTERA University Trainer Kit	1
3	CPLD kits for COOL RUNNER	5
4	XUP spartan 3E with USB Programmable cable	10
5	XLINX ISE Foundation V82i VLSI Design Software	1
6	Computer: HP PRO 3330 -I3 Processors, Monitor: HP 18.5wide Colour	12
7	COMPUTER: HP280 G I3 PROCESSORS	24
8	MULTIVENDER UNIVERSAL DEMO BOARD	2
9	CADENCE University PG Bundle (UG & PG Bundle Analog & Digital FE & BE) No of Licenses: 20, No of years: 3 years-SOFTWARE	1
10	LCD Projector	1
11	UPS-6KVA with Batteries	1